

Notice of References Cited	Application/Control No. 10/028,067		Applicant(s)/Patent Under Reexamination SHI, TONG	
	Examiner Glenda P. Rodriguez		Art Unit 2651	Page 1 of 2

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,501,610	12-2002	Sugawara et al.	360/65
	B	US-			
	C	US-			
	D	US-			
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	F	US-			
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	I	US-			
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	K	US-			
	L	US-			
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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"A 110 MHz 350 mW 0.6 um CMOS 16-State Generalized-Target Viterbi Detector for Disk Drive Read Channels" by Sridharan et al., IEEE Transactions on Solid-State Circuits, Vol. 35, No. 3, March 2000, Pages 362-370
	V	"Multi-level Decision Feedback Equalization: An Efficient Realization of FDS/DF" by Kenney et al., IEEE Transactions on Magnetics, Vol. 31, No. 2, March 1995, Pages 1115-1120
	W	"A 200-MSample/s Trellis-Coded PRML Read/Write Channel with Analog Adaptive Equalizer and Digital Servo" by Alini et al., IEEE Journal of Solid-State Circuits, Vol. 22, No. 11, November 1997, Pages 1824-1838
	X	"Adaptive Equalization" by Proakis, "Digital Communications", Third Edition, Pages 637-674

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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	W	
	X	

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